

AMENDMENT AND RESPONSE**PAGE 2**

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Title: CLOCK RECOVERY MECHANISM

Amendments to the Claims:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of claims:

Claims 1-8. Cancelled

9. (Original) A method of recovering a clock signal for a buffer, the method comprising:
buffering incoming data in a buffer;
formulating a frequency estimate based on a data rate of the incoming data;
providing the frequency estimate and the incoming data to an asynchronous transfer mode (ATM) segmentation and reassembly sublayer;
encoding the frequency estimate into ATM traffic;
sending the ATM traffic to a receiver; and
seeding a frequency lock algorithm with the estimate.
10. (Original) The method of claim 9, wherein seeding a frequency lock algorithm comprises:
recovering the frequency estimate from the received ATM traffic; and
decoding the estimate.
11. (Original) The method of claim 9, and further comprising:
adjusting a depth of the input buffer to facilitate throughput management.
12. (Original) The method of claim 9, and further comprising:
buffering received data in an output buffer; and
adjusting a depth of the output buffer to match the inflow of data at the input buffer.

Claims 13-30. Cancelled